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| **Program - CSE** | | **Semester – IV** | |  | |
| **Course – MBS** | | **Course code - MBS190806** | |  | |
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| **Ch 1 – The 8086 microprocessor** | | | | |
|  | **Questions** | | **Answer from** | **Marks** |
| Q.1 | List the salient features of 8086 Microprocessor | | PPT | 3/4 |
| Q.2 | Define term Microprocessor - | | PPT | 4 |
| Q.3 | Draw and explain architecture of 8086 /List the functional parts of 8086, explain one in detail. | | PPT | 3/4/6 |
| Q.4 | Define term pipelining and explain any two advantages of pipelining | | PPT | 4 |
| Q.5 | Draw flag register of 8086 and explain | | PPT | 4/6 |
| Q.6 | Explain special functions of general purpose registers or List all 16 bit registers of 8086 | | PPT | 3/4/6 |
| Q.7 | Explain use of any pin or signal of 8086 | | PPT | 4 |
| Q.8 | Draw and explain memory bank of 8086. | | PPT | 6 |
| Q.9 | Draw and explain memory segmentation of 8086 | | --- | 6 |
| Q.10 | Draw and explain significance of queue in 8086 | | PPT | 6 |
| Q.11 | When pipeline fails? | | PPT | 3 |
| Q.12 | Explain overflow flag with the help of example | | PPT | 6 |
| Q.13 | Draw functional pin diagram of 8086 only diagram | | PPT | 3 |
| Q.14 | Problems on offset address ,flags etc | |  |  |
| Q.15 | Define segment Registers and memory pointer. Give example | | PPT | 3/4 |
| Q.16 | State the importance of memory segmentation. Draw the memory segmentation with example. | |  |  |
| Q.17 | Define the concept of aligned and misaligned data | |  | 3/4 |

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| **Ch 2 – Instruction set & timing diagram of 8086** | | |  |
|  | **Questions** | **Answer from** | Marks |
| Q.1 | Explain any specific individual / group of instructions for 8086 | PPT | 3/4/6 |
| Q.2 | Programs based on instructions. |  |  |
| Q.3 | Explain addressing modes of 8086 (each for 3mks )  1mk -for explanation  2mk – for example | PPT | 3/6 |